

ABSTRACT

The invention relates to a method for automatic recognition of simulation configurations of integrated circuits under design comprising at least two components connected to one another directly or indirectly, for the functional verification of the integrated circuits through simulation tests. The method ~~comprises~~includes ~~[[.]]~~ ~~[[.]]~~ a step for the acquisition of a simulation configuration by a server manager (14), ~~[[.]]~~ a step for the sending of a request by a client manager (11), to the server manager (14), ~~[[.]]~~ a step for sending a response by the server manager (14) to the client manager (11), and ~~[[.]]~~ a step for the comparison by the client manager (11) of the response with the requirements of the test, followed by a step for the disabling, activation and/or modification of certain parts of the test by the client manager (11) in order to adapt the test to the configuration or signaling an error if the test cannot be adapted to the configuration.